

L Number	Hits	Search Text	DB	Time stamp
1	21077	ASIC	USPAT; EPO; JPO; DERWENT	2004/06/08 13:58
2	2001	ASIC near3 design\$3	USPAT; EPO; JPO; DERWENT	2004/06/08 13:59
3	938	ASIC adj3 design	USPAT; EPO; JPO; DERWENT	2004/06/08 13:59
4	723	ASIC adj1 design	USPAT; EPO; JPO; DERWENT	2004/06/08 13:59
5	240	ASIC adj1 designs	USPAT; EPO; JPO; DERWENT	2004/06/08 13:59
6	370243	(volatile adj2 memory) or RAM or DRAM	USPAT; EPO; JPO; DERWENT	2004/06/08 14:01
7	3542	((volatile adj2 memory) or RAM or DRAM) near3 check\$3	USPAT; EPO; JPO; DERWENT	2004/06/08 14:01
8	872	((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same (processor or CPU)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:02
9	405	((volatile adj2 memory) or RAM or DRAM) near3 check\$3) near10 (processor or CPU)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:02
10	297	((volatile adj2 memory) or RAM or DRAM) near3 check\$3) near5 (processor or CPU)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:02
11	107	((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same (processor or CPU) same test\$3	USPAT; EPO; JPO; DERWENT	2004/06/08 14:03
12	0	((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same (processor or CPU) same test\$3) and (ASIC near3 design\$3)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:03
13	2	((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same (processor or CPU) same test\$3) and ASIC	USPAT; EPO; JPO; DERWENT	2004/06/08 14:22
14	533	((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same test\$3)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:22
15	0	((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same (soft near2 test\$3)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:23
16	0	((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same (soft near2 error\$1) same test\$3	USPAT; EPO; JPO; DERWENT	2004/06/08 14:23
17	57	((volatile adj2 memory) or RAM or DRAM) near3 checker)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:24
19	4	((volatile adj2 memory) or RAM or DRAM) near3 checker) same self-test\$3	USPAT; EPO; JPO; DERWENT	2004/06/08 14:24
18	15	((volatile adj2 memory) or RAM or DRAM) near3 checker) same test\$3	USPAT; EPO; JPO; DERWENT	2004/06/08 14:33
20	31	((volatile adj2 memory) or RAM or DRAM) adj3 checker)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:34
21	367	((volatile adj2 memory) or RAM or DRAM) adj3 (checker or detector))	USPAT; EPO; JPO; DERWENT	2004/06/08 14:38
22	200	((volatile adj2 memory) or RAM or DRAM) adj2 (checker or detector))	USPAT; EPO; JPO; DERWENT	2004/06/08 14:35
23	47	((volatile adj2 memory) or RAM or DRAM) adj1 (checker or detector))	USPAT; EPO; JPO; DERWENT	2004/06/08 14:35

24	13	((volatile adj2 memory) or RAM or DRAM) adj3 (checker or detector)) and ((test adj2 data) or (test adj2 code\$1) or (test adj2 information))	USPAT; EPO; JPO; DERWENT	2004/06/08 14:45
25	49	((volatile adj2 memory) or RAM or DRAM) same (checker or detector)) same ((test adj2 data) or (test adj2 code\$1) or (test adj2 information))	USPAT; EPO; JPO; DERWENT	2004/06/08 14:44
26	7	((volatile adj2 memory) or RAM or DRAM) same (checker or detector)) same ((test adj2 data) or (test adj2 code\$1) or (test adj2 information)) same processor	USPAT; EPO; JPO; DERWENT	2004/06/08 14:44
29	106	detect\$3 adj3 (soft adj2 errors)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:47
30	147	detect\$3 adj3 (soft adj2 error\$1)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:56
31	13	(detect\$3 adj3 (soft adj2 error\$1)) same ((volatile adj2 memory) or RAM or DRAM)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:48
32	2988	taylor-r\$.in.	USPAT; EPO; JPO; DERWENT	2004/06/08 14:53
33	1	taylor-r\$.in. and (((volatile adj2 memory) or RAM or DRAM) adj3 (checker or detector))	USPAT; EPO; JPO; DERWENT	2004/06/08 14:54
34	1	taylor-r\$.in. and (((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same test\$3)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:54
35	0	taylor-r\$.in. and (ASIC near3 design\$3)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:54
36	143	(error near1 detect\$3) near5 (soft adj2 error\$1)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:56
37	159	(error\$1 near1 detect\$3) near5 (soft adj2 error\$1)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:56
38	15	((error\$1 near1 detect\$3) near5 (soft adj2 error\$1)) same ((volatile adj2 memory) or RAM or DRAM)	USPAT; EPO; JPO; DERWENT	2004/06/08 15:01
39	159	((error\$1 near1 detect\$3) near5 (soft adj2 error\$1))	USPAT; EPO; JPO; DERWENT	2004/06/08 15:02
40	11	((error\$1 near1 detect\$3) near5 (soft adj2 error\$1)) same test\$3	USPAT; EPO; JPO; DERWENT	2004/06/08 15:41
41	207	DRAM near5 SRAM near5 processor	USPAT; EPO; JPO; DERWENT	2004/06/08 15:43
42	2	(DRAM near5 SRAM near5 processor) same printer	USPAT; EPO; JPO; DERWENT	2004/06/08 15:44
43	17	(DRAM near5 SRAM near5 processor) and printer	USPAT; EPO; JPO; DERWENT	2004/06/08 15:44

L Number	Hits	Search Text	DB	Time stamp
1	0	"recovery adj2 module"	USPAT; EPO; JPO; DERWENT	2004/06/08 16:41
2	474	recovery adj2 module	USPAT; EPO; JPO; DERWENT	2004/06/08 16:42
3	10	(recovery adj2 module) same self-test\$3	USPAT; EPO; JPO; DERWENT	2004/06/08 16:44
4	17	(recovery near2 error) same self-test\$3	USPAT; EPO; JPO; DERWENT	2004/06/08 16:46
5	204	(recovery near2 error) and self-test\$3 and ram	USPAT; EPO; JPO; DERWENT	2004/06/08 16:46
6	195	(recovery near2 error) and self-test\$3 and ram and processor	USPAT; EPO; JPO; DERWENT	2004/06/08 16:47
7	1	(recovery near2 error) and self-test\$3 and ram and processor and re-initialization	USPAT; EPO; JPO; DERWENT	2004/06/08 16:47
8	177	(recovery near2 error) and self-test\$3 and ram and processor and initialization	USPAT; EPO; JPO; DERWENT	2004/06/08 16:47
9	1	(recovery near2 error) and self-test\$3 and ram and processor and reinitialization	USPAT; EPO; JPO; DERWENT	2004/06/08 16:50
10	8056	"printer controller"	USPAT; EPO; JPO; DERWENT	2004/06/08 16:51
11	0	"printer controller" and ((recovery near2 error) and self-test\$3 and ram and processor and initialization)	USPAT; EPO; JPO; DERWENT	2004/06/08 16:51
12	1	"printer controller" and (recovery adj2 module)	USPAT; EPO; JPO; DERWENT	2004/06/08 16:52
13	33	"self-test" same CRC	USPAT; EPO; JPO; DERWENT	2004/06/08 16:56
14	1247	(self-test\$3 or (self adj2 test\$3)) near3 (RAM or DRAM or memory)	USPAT; EPO; JPO; DERWENT	2004/06/08 16:57
15	802	(self-test\$3 or (self adj2 test\$3)) near2 (RAM or DRAM or memory)	USPAT; EPO; JPO; DERWENT	2004/06/08 16:57
16	195	(self-test\$3 or (self adj2 test\$3)) near2 (RAM or DRAM)	USPAT; EPO; JPO; DERWENT	2004/06/08 16:58
17	144	(self-test\$3 or (self adj2 test\$3)) adj2 (RAM or DRAM)	USPAT; EPO; JPO; DERWENT	2004/06/08 17:01
18	2	((self-test\$3 or (self adj2 test\$3)) adj2 (RAM or DRAM)) same recovery	USPAT; EPO; JPO; DERWENT	2004/06/08 16:59
19	16	((self-test\$3 or (self adj2 test\$3)) adj2 (RAM or DRAM)) and recovery	USPAT; EPO; JPO; DERWENT	2004/06/08 17:01
20	19	((self-test\$3 or (self adj2 test\$3)) adj2 (RAM or DRAM)) same processor	USPAT; EPO; JPO; DERWENT	2004/06/08 17:01